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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EV026158363US

Application Serial No. .... 09/810,595  
Filing Date .... March 15, 2001  
Inventor .... Belford T. Coursey  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... Unknown  
Examiner .... Unknown  
Attorney's Docket No. .... MI22-1660  
Title: Memory Circuitry, and Dynamic Random Access Memory Circuitry

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 2-25-02

By: 

Mark S. Matkin  
Reg. No. 32,268

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